



FILE No .A28838-I-A - 070421.1206
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hidemi Takasu
Serial No. : 09/212,915 Examiner : M. Estrada
Filed : December 16, 1998 Group Art Unit: 2823
For : PROCESS FOR FABRICATING A SEMICONDUCTOR DEVICE

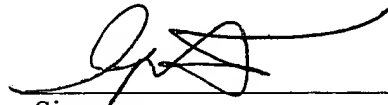
SUBMISSION OF CORRECTED DRAWINGS

I hereby certify that this paper is being deposited
with the United States Postal Service as first class mail in
an envelope addressed to: Commissioner for Patents, P.O.
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March 8, 2005
Date of Deposit

Gary M. Butter
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33,841
PTO Registration No


Signature

March 8, 2005
Date of Signature

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
BOX ISSUE FEE

Sir:

In response to the Notice of Draftsperson's Patent Drawing Review mailed August
30, 2000 and the Notice of Allowability mailed December 30, 2004, please substitute the
attached six (6) sheets of drawing for the drawings submitted with the application.

Applicant does not believe that this submission requires a fee. However, if for any reason a fee is due, the Commissioner is hereby authorized to charge payment of any fees required in connection with this submission to Deposit Account No. 02-4377. A duplicate copy of this sheet is transmitted herewith.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Gary M. Butter', is written over a horizontal line.

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Enclosures